

DOCKET NO.: STM107-02021

PATENT

Customer No.: 23990

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of : FRANCK BADETS, ET AL.

U.S. Serial No. : 10/603,579

Filed : June 25, 2002

For : VARIABLE PHASE-SHIFTING CIRCUIT, PHASE  
INTERPOLATOR INCORPORATING IT, AND DIGITAL  
FREQUENCY SYNTHESIZER INCORPORATING SUCH AN  
INTERPOLATOR

Group No. : 2631

Examiner : (Not Yet Assigned)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INFORMATION DISCLOSURE STATEMENT**

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO/SB/08. These references were located in a European Search Report issued in the related French Patent Application No. FR 0207856, a copy of which is attached hereto. Copies of the listed references are submitted herewith.

**U.S. Patents**

<b><u>PATENT NO.</u></b>	<b><u>INVENTOR</u></b>	<b><u>DATE</u></b>
4,916,412	Nordholt, et al.	04/10/1990
6,396,360	Cai	05/28/2002
6,414,528	Usui	07/02/2002

**Foreign Patents**

<b><u>PATENT NO.</u></b>	<b><u>COUNTRY</u></b>	<b><u>DATE</u></b>
EP 0 326 202	EPO	08/02/1989
WO 00/65717	PCT	11/02/2000

**Publications**

RICHTER, R., et al. "A VIRTUAL CLOCK ENHANCEMENT METHOD FOR DDS USING AN ANALOG DELAY LINE"; IEEE Journal of Solid state Circuits, Vol. 36, No. 7, July 2001; pages 1158-1161;

NOSAKA, H., et al.; "A LOW-POWER DIRECT DIGITAL SYNTHESIZER USING A SELF-ADJUSTING PHASE-INTERPOLATION TECHNIQUE"; IEEE Journal of Solid-State Circuits, Vol. 36, No. 8, August 2001; pages 1281-1285;

Chapitre 3; Oscillateurs Synchrones; pages 99-126


Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

DOCKET No.: STMI07-02021  
SERIAL NO.: 10/603,579  
PATENT

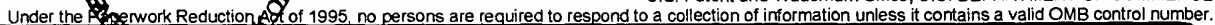
Respectfully submitted,

DAVIS MUNCK, P.C.

Date: Sept. 25, 2013

  
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*(Use as many sheets as necessary)*

Sheet 1 of 2

Application Number	10/603,579
Filing Date	June 25, 2003
First Named Inventor	Franck Badets, et al.
Art Unit	2631
Examiner Name	Not yet assigned
Attorney Docket Number	STM07-02021

[illegible]

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
	AD	EP 0 326 202	08/02/1989	Catena		
	AE	WO 00/65717	11/02/2000	Seiko Epson Corp (JP)	(Abstract only)	

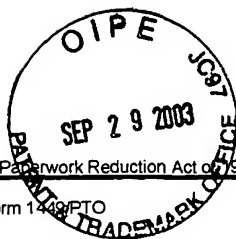
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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PTO/SB/08B (08-03)

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Substitute for form 1449 PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Complete if Known**

Application Number	10/603,579
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Art Unit	2631
Examiner Name	Not yet assigned
Attorney Docket Number	STMI07-02021

Sheet 2 of 2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	AF	RICHTER, R., et al. "A VIRTUAL CLOCK ENHANCEMENT METHOD FOR DDS USING AN ANALOG DELAY LINE"; IEEE Journal of Solid state Circuits, Vol. 36, No. 7, July 2001; pages 1158-1161;	
	AG	NOSAKA, H., et al.; "A LOW-POWER DIRECT DIGITAL SYNTHESIZER USING A SELF-ADJUSTING PHASE-INTERPOLATION TECHNIQUE"; IEEE Journal of Solid-State Circuits, Vol. 36, No. 8, August 2001; pages 1281-1285;	
	AH	Chapitre 3; Oscillateurs Synchrones; pages 99-126	

Examiner  
SignatureDate  
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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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